



PATENT

Applicants: Rajat Aggarwal et al.
Assignee: Xilinx, Inc.
Title: Method, System, and Apparatus for Incremental Design in Programmable Logic Devices Using Floorplanning
Serial No.: 10/812,550 File Date: 03-29-04
Examiner: Paul Dinh Art Unit: 2825
Docket No.: X-1490 US Conf. No.: 3439

Mail Stop AMENDMENT
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INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the two (2) references listed in the attached Substitute for Form 1449A/PTO. A copy of each is enclosed herein.

The enclosed references are being cited after the receipt of a first office action but prior to any final action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

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Respectfully submitted,

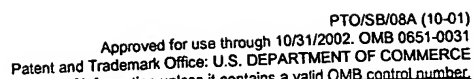
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Kim Kanzaki, Ph.D.
Attorney for Applicants
Reg. No. 37,652

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Application / Conf. No.	10/812,550 / 3439
Filing Date	March 29, 2004
First Named Inventor	Rajat Aggarwal
Art Unit	2825
Examiner Name	Paul Dinh
Attorney Docket Number	X-1490 US

OTHER -- NON PATENT LITERATURE DOCUMENTS

[illegible]

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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¹ Applicant's unique citation designation number. ² Applicant is to place a check mark here if English language Translation is attached.

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